



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )

ELIYAHOU HARARI et al. )

Serial No.: 08/789,421 )

Filed: January 29, 1997 )

For: FLASH EEPROM SYSTEM )

Group Art Unit: 2785

Examiner: L. Hua

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**PRELIMINARY AMENDMENT TO**  
**SECOND CONTINUED PROSECUTION APPLICATION**

Sir:

Please amend independent claims 63, 115, 124 and 141 of the above-identified patent application, as follows:

~~63.~~ (Fourth Amendment) A method of operating a bulk storage memory with a host processor, wherein the bulk storage memory includes an array of non-volatile floating gate memory cells, comprising:

storing, within defined non-overlapping groups of said array cells that are erasable together as a unit, [sectors of] user data and associated overhead data, at least some of said overhead data being generated within the bulk storage memory to include information of [at least one of (a)] the same individual memory array cell groups in which the overhead data are stored [or (b) the user data stored in the same individual memory array cell groups as the overhead data],